AMENDMENTS TO THE CLAIMS

1. (Cancelled)

2. (Currently Amended) The system of claim [[1]] 53, wherein said display means further comprises a graphical user interface.

3. (Currently Amended) The system of claim [[1]] 53, wherein said bit burst analysis, network latency, data delivery success and frame size distribution information is derived from said first and second communication device by a network management system.

4. (Cancelled)

5. (Cancelled)

6. (Currently Amended) The system of claim [[5]] 50, wherein said display means further comprises a graphical user interface.

7. (Cancelled)

8. (Cancelled)

9. (Currently Amended) The method of claim [[8]] 51, further comprising the step of:

collecting in said network management system said plurality of network performance parameter ~~views~~ information from said first and said second communication devices.

10. (Cancelled)

11. (Cancelled)

12. (Currently Amended) The program of claim [[11]] 52, further comprising logic configured to perform the step of:

collecting in said network management system said plurality of network performance parameter views from said first and said second communication devices.

13. (Cancelled)

14. (Cancelled)

15. (Cancelled)

16. (Currently Amended) The system of claim [[1]] 53 , wherein said virtual circuit is a permanent virtual circuit.

17. (Currently Amended) The system of claim [[1]] 53 , wherein said virtual circuit is a switched virtual circuit.

18. (Cancelled)

19. (Currently Amended) The method of claim [[8]] 51, further comprising the step of displaying said bit burst analysis, network latency, data delivery success and frame size distribution views simultaneously.

20. (Currently Amended) The program of claim [[11]] 52, further comprising logic configured to perform the step of displaying said bit burst analysis, network latency, data delivery success and frame size distribution views simultaneously.

21. (Currently Amended) The system of claim [[1]] 53, further comprising:

means for collecting the information from the first communication device over a first secondary management channel and from the second communication device over a second secondary management channel.

22. (Currently Amended) The method of claim [[8]] 51, further comprising:

collecting the plurality of network performance information from the first communication device over a first secondary management channel and from the second communication device over a second secondary management channel.

23. (Currently Amended) The computer readable medium of claim [[11]] 52, further comprising the step of:

collecting the plurality of network performance information from the first communication device over a first secondary management channel and from the second communication device over a second secondary management channel.

24. (Cancelled)

25. (Currently Amended) The system of claim [[24]] 50, wherein said display means further comprises a graphical user interface.

26. (Currently Amended) The system of claim [[24]] 50, wherein said bit burst analysis, network latency, data delivery success and frame size distribution information is derived from said first and second communication device by a network management system.

27. (Currently Amended) The system of claim [[24]] 50 , wherein said virtual circuit is a permanent virtual circuit.

28. (Currently Amended) The system of claim [[24]] 50, wherein said virtual circuit is a switched virtual circuit.

29. (Cancelled)

30. (Cancelled)

31. (Currently Amended) The method of claim [[30]] 51, wherein said virtual circuit is a permanent virtual circuit.

32. (Currently Amended) The method of claim [[30]] 51, wherein said virtual circuit is a switched virtual circuit.

33. (Currently Amended) The ~~method~~ system of claim [[30]] 50, ~~further comprising the step of:~~

~~collecting in said network management system~~ wherein said plurality of network performance parameter views are collected from said first and said second communication devices.

34. (Cancelled)

35. (Currently Amended) The ~~method~~ system of claim [[30]] 50, ~~further comprising the step of displaying~~ wherein said display means presents said bit burst analysis, network latency, data delivery success and frame size distribution views simultaneously.

36. (Cancelled)

37. (Currently Amended) The computer readable medium of claim [[36]] 52, wherein said virtual circuit is a permanent virtual circuit.

38. (Currently Amended) The computer readable medium of claim [[36]] 52, wherein said virtual circuit is a switched virtual circuit.

39. (Cancelled)

40. (Cancelled)

41. (Cancelled)

42. (Cancelled)

43. (Currently Amended) The system of claim [[42]] 56, wherein the poller is further configured to poll the first communication device over a first secondary management channel and to poll the second communication device over a second secondary management channel.

44. (Currently Amended) The system of claim [[42]] 56, further comprising:

a statistics database configured to store the plurality of network performance information.

45. (Currently Amended) The system of claim [[42]] 56, further comprising:

a formatter configured to prepare the report for visual presentation.

46. (Currently Amended) The system of claim [[42]] 56, further comprising:

means for setting the rate at which the poller operates.

47. (Currently Amended) The system of claim [[42]] 56, wherein said virtual circuit is a permanent virtual circuit.

48. (Currently Amended) The system of claim [[42]] 56, wherein said virtual circuit is a switched virtual circuit.

49. (Cancelled)

50. (Currently Amended) ~~The system of claim 5,~~ In a communication environment having at least a first and a second communication device, said communication devices configured to couple user devices to the network, and a network management system, a system for displaying network performance information, comprising:

a plurality of network performance parameter views, comprising a bit burst analysis view, a network latency view, a data delivery success view and a frame size distribution view, wherein said views are associated with a virtual circuit between the first and the second communication device; and

display means for presenting to a user said plurality of network performance parameter views,

where said bit burst analysis view comprises a plurality of bit burst counters, each of said bit burst counters counting one or more bit bursts that was placed into one of a plurality of burst categories, wherein each of said bit burst counters counts the one or more bit bursts that was placed into one of the plurality of burst categories during a sliding window time interval, the time interval synchronized to begin with detection of the first of the one or more bit bursts.

51. (Currently Amended) ~~The method of claim 8,~~ A method for displaying network performance parameters in a network comprising a network management system and at least a first and a second communication device, said communication devices configured to couple user devices to the network, the method comprising the steps of:

collecting a plurality of network performance information including bit burst analysis information, network latency information, data delivery success information, and frame size distribution information, each of said plurality associated with a virtual circuit between the first and the second communication device; and

displaying views of said bit burst analysis, said network latency, said data delivery success, and said frame size distribution information,

where said bit burst analysis information comprises a plurality of bit burst counters, each of said bit burst counters counting one or more bit bursts that was placed into one of a plurality of burst categories, wherein each of said bit burst counters counts the one or more bit bursts that was placed into one of the plurality of burst categories during a sliding window time interval, the time interval synchronized to begin with detection of the first of the one or more bit bursts.

52. (Currently Amended) ~~The program of claim 11,~~ A computer readable medium having a program for displaying network performance parameters in a network comprising a network management system and at least two communication devices, said communication devices configured to couple user devices to the network, the program comprising logic configured to perform the steps of:

collecting a plurality of network performance information including bit burst analysis information, network latency information, data delivery success information, and frame size distribution information, each of said plurality associated with a virtual circuit between the first and the second communication device; and

displaying views of said bit burst analysis, said network latency, said data delivery success, and said frame size distribution information,

where said bit burst analysis information comprises a plurality of bit burst counters, each of said bit burst counters counting one or more bit bursts that was placed into one of a plurality of burst categories, wherein each of said bit burst counters counts the one or more bit bursts that was placed into one of the plurality of burst categories during a sliding window time interval, the time interval synchronized to begin with detection of the first of the one or more bit bursts.

53. (Currently Amended) ~~The system of claim 24,~~ A system for displaying network performance parameters, comprising:

means for collecting, from a first and a second communication device, bit burst analysis information, network latency information, data delivery success information and frame size distribution information associated with a virtual circuit between the first and the second communication device, said first and second communication device each being configured to couple at least one user device to the network; and

display means for displaying said bit burst analysis, network latency, data delivery success and frame size distribution information,

where said bit burst analysis information comprises a plurality of bit burst counters, each of said bit burst counters counting one or more bit bursts that was placed into one of a plurality of burst categories, wherein each of said bit burst counters counts the one or more bit bursts that was placed into one of the plurality of burst categories during a sliding window time interval, the time interval synchronized to begin with detection of the first of the one or more bit bursts.

54. (Cancelled)

55. (Cancelled)

56. (Currently Amended) ~~The system of claim 42,~~ A system for displaying network performance parameters associated with a first and a second communication device, comprising:

a poller means for polling the first and the second communication device for a plurality of network performance information comprising bit burst analysis information, network latency information, data delivery success information, and frame size distribution information, each of said plurality associated with a virtual circuit between the first and the second communication

device, the first and second communication device being configured to couple at least one user device to the network;

an analyzer means for producing a report of the plurality of network performance information; and

a display module means for displaying the report, wherein each of said bit burst counters counts the one or more bit bursts that was placed into one of the plurality of burst categories during a sliding window time interval, the time interval synchronized to begin with detection of the first of the one or more bit bursts.

57. (New) The computer readable medium of claim 52, wherein said bit burst analysis, network latency, data delivery success and frame size distribution information is derived from said first and second communication device by a network management system.

58. (New) The method of claim 51, wherein said bit burst analysis, network latency, data delivery success and frame size distribution information is derived from said first and second communication device by a network management system.

59. (New) The system of claim 56, wherein said display module means displays said bit burst analysis, network latency, data delivery success and frame size distribution information simultaneously.